

Notice of References Cited	Application/Control No. 10/644,633	Applicant(s)/Patent Under Reexamination KOLDIAEV ET AL.	
	Examiner Johannes P Mondt	Art Unit 2826	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2002/0146879	10-2002	Fu et al.	438/230
*	B	US-6,323,519	11-2001	Gardner et al.	257/336
	C	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S., "Silicon Processing for the VLSI Era", Volume 2 - Process Integration, Lattice Press, Sunset Beach, California (1990) (ISBN No.: 0-961672-4-5), in particular pages 201-208.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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